

Our Docket No: 42P13230D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applica	tion of:)		
Shafquat Ahmed et al.)	Examiner:	Booth, Richard
Serial No:	10/619,961)	Art Unit:	2812
Filed:	July 15, 2003)		
For: Method of Forming Transistor Having) Insulating Spacers On Gate Sidewalls)				
PRELIMINARY AMENDMENT				
Mail Stop: RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450				
Sir:				
Prior to examination of the present RCE application, the Applicants respectfully				
request that the Examiner enter the following amendments and consider the following				
remarks.				
FIRST CLASS CERTIFICATE OF MAILING				
I hereby certify that I am causing the above-referenced correspondence to be deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and that this paper or fee has been addressed to the Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450				
		January 30, 200		
		Date of Deposi	t	
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	ah Schwener	<u> </u>	1/30/06	
Signature			Date	